

Date: June 12, 2006



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

I hereby certify that this paper is being deposited on this date with the

U.S. Postal Service as first class mail addressed to the Commissioner for

Patents, P.Q. 14507 Alexandria, VA 22313-1450

Patent Application

5

Applicant(s): Brennan et al.

Case:

3-82-47

Serial No.: Filing Date:

10/788,678 February 27, 2004

10 Group: 2814

Examiner:

Phat X. Cao

Title:

Techniques for Reducing Bowing in Power Transistor Devices

Signature:

RESPONSE AFTER FINAL REJECTION

cmsidne N 6/26/020

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

25

Sir:

In response to the outstanding final Office Action, dated April 12, 2006, Applicants submit the following remarks.

30